

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10081546	FILING DATE 02/21/2002	CLASS 365	SUBCLASS 233	GAU 2818	EXAMINER MAI, SON
-----------------------------	----------------------------------	---------------------	------------------------	--------------------	-----------------------------

****APPLICANTS:** Lee Jung-bae; La One-gyun;

****CONTINUING DATA VERIFIED:**

NONE - file

**** FOREIGN APPLICATIONS VERIFIED:**

REPUBLIC OF KOREA 2001-24044 03/05/2001 ✓

REPUBLIC OF KOREA 2001-44065 07/21/2001 ✓

PG-PUB ☐ DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☒ yes ☐ no

35 USC 119 conditions met ☒ yes ☐ no

Verified and Acknowledged Examiners's initials *SM*

ATTORNEY DOCKET NO

8729-214 (SS-15661-US)

TITLE : Data input circuit and method for synchronous semiconductor memory device

U.S. DEPT. OF COMM. / PAT. & TM-FTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED					
		Assistant Examiner	<table border="1"> <tr> <td>Total Claims</td> <td>Print Claim for O.G.</td> </tr> <tr> <td></td> <td></td> </tr> </table>	Total Claims	Print Claim for O.G.		
Total Claims	Print Claim for O.G.						
ISSUE FEE		DRAWING					
Amount Due	Date Paid	Sheets Drawg.	Figs Drawg.				
			Print Fig.				
		Primary Examiner	Application Examiner				
<input type="checkbox"/> TERMINAL		PREPARED FOR ISSUE					
DISCLAIMER		<p>WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.</p>					

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)